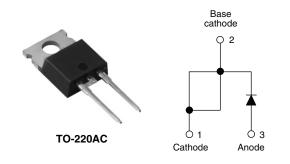
Vishay High Power Products

Ultrafast Rectifier, 15 A FRED Pt[™]



PRODUCT SUMMARY					
t _{rr} 35 ns					
I _{F(AV)}	15 A				
V _R	200 V				

FEATURES

- Ultrafast recovery time
- · Low forward voltage drop
- Low leakage current
- 175 °C operating junction temperature
- Lead (Pb)-free ("PbF" suffix)
- Designed and qualified for industrial level

DESCRIPTION/APPLICATIONS

MUR.. series are the state of the art ultrafast recovery rectifiers specifically designed with optimized performance of forward voltage drop and ultrafast recovery time.

The planar structure and the platinum doped life time control, guarantee the best overall performance, ruggedness and reliability characteristics.

These devices are intended for use in the output rectification stage of SMPS, UPS, dc-to-dc converters as well as freewheeling diode in low voltage inverters and chopper motor drives.

Their extremely optimized stored charge and low recovery current minimize the switching losses and reduce over dissipation in the switching element and snubbers.

ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDITIONS	MAX.	UNITS	
Peak repetitive reverse voltage	V _{RRM}		200	V	
Average rectified forward current	I _{F(AV)}	Total device, rated V _R , T _C = 150 °C	15		
Non-repetitive peak surge current	I _{FSM}		200	А	
Peak repetitive forward current	I _{FM}	Rated V _R , square wave, 20 kHz, T _C = 150 °C	30		
Operating junction and storage temperatures	T _J , T _{Stg}		- 65 to 175	°C	

ELECTRICAL SPECIFICATIONS (T _J = 25 °C unless otherwise specified)						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Breakdown voltage, blocking voltage	V _{BR} , V _R	I _R = 100 μA	200	-	-	
Forward voltage	VF	I _F = 15 A	-	-	1.05	V
Forward voltage V _F		I _F = 15 A, T _J = 150 °C	-	-	0.85	
Reverse leakage current I _R		$V_{R} = V_{R}$ rated	-	-	10	
		$T_J = 150 \text{ °C}, V_R = V_R \text{ rated}$	-	-	500	- μΑ
Junction capacitance	CT	V _R = 200 V	-	55	-	pF
Series inductance	L _S	Measured lead to lead 5 mm from package body	-	8.0	-	nH

* Pb containing terminations are not RoHS compliant, exemptions may apply

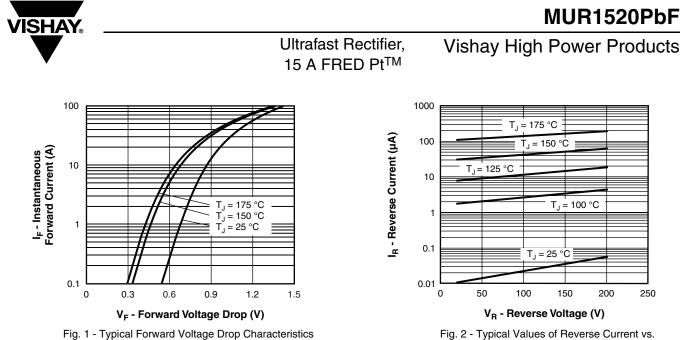
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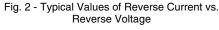
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DYNAMIC RECOVERY CHARACTERISTICS ($T_J = 25$ °C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNITS
		$I_F = 1.0 \text{ A}, \text{ d}I_F/\text{d}t = 50 \text{ A}/\mu\text{s}, V_R = 30 \text{ V}$		-	-	35	
Reverse recovery time	t _{rr}	T _J = 25 °C		-	22	-	ns
		T _J = 125 °C		-	39	-	
Pools recovery ourrent	I _{RRM}	T _J = 25 °C	I _F = 15 A dI _F /dt = 200 A/μs	-	1.6	-	А
Peak recovery current I _{RRM}		T _J = 125 °C	$V_{\rm R} = 160 \text{ V}$	-	4.1	-	A
Reverse recovery charge Q _{rr}		T _J = 25 °C		-	19	-	nC
	T _J = 125 °C		-	90	-	nc.	

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Maximum junction and storage temperature range	T _J , T _{Stg}		- 65	-	175	°C
Thermal resistance, junction to case	R _{thJC}		-	-	1.5	
Thermal resistance, junction to ambient	R _{thJA}		-	-	50	°C/W
Thermal resistance, case to heatsink	R _{thCS}	Mounting surface, flat, smooth and greased	-	0.5	-	-
Weight			-	2.0	-	g
weight			-	0.07	-	oz.
Mounting torque			6.0 (5.0)	-	12 (10)	kgf ⋅ cm (lbf ⋅ in)
Marking device		Case style TO-220AC		MUR	1520	•





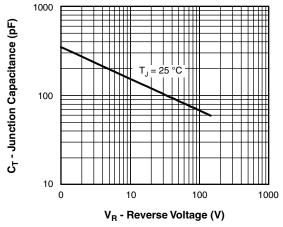


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

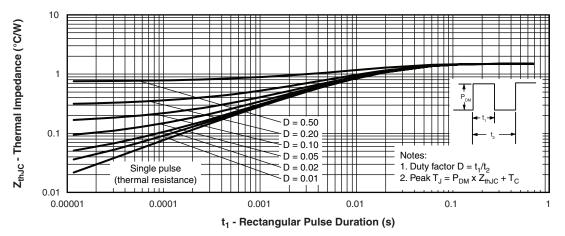


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics

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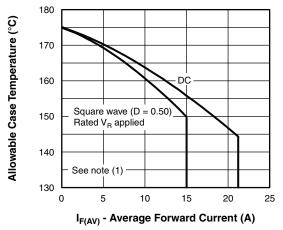
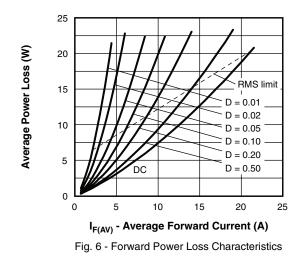


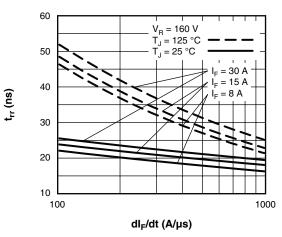
Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current



Note

⁽¹⁾ Formula used: $T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}$;

 $\begin{array}{l} \mathsf{Pd} = \mathsf{Forward} \ \mathsf{power} \ \mathsf{loss} = \mathsf{I}_{\mathsf{F}(\mathsf{AV})} \times \mathsf{V}_{\mathsf{FM}} \ \mathsf{at} \ (\mathsf{I}_{\mathsf{F}(\mathsf{AV})}/\mathsf{D}) \ (\mathsf{see} \ \mathsf{fig.} \ \mathsf{6}); \\ \mathsf{Pd}_{\mathsf{REV}} = \mathsf{Inverse} \ \mathsf{power} \ \mathsf{loss} = \mathsf{V}_{\mathsf{R1}} \times \mathsf{I}_{\mathsf{R}} \ (\mathsf{1} - \mathsf{D}); \ \mathsf{I}_{\mathsf{R}} \ \mathsf{at} \ \mathsf{V}_{\mathsf{R1}} = \mathsf{Rated} \ \mathsf{V}_{\mathsf{R}} \end{array}$



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Fig. 7 - Typical Reverse Recovery Time vs. dl_F/dt

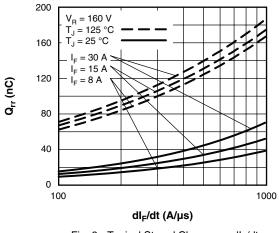


Fig. 8 - Typical Stored Charge vs. dI_F/dt

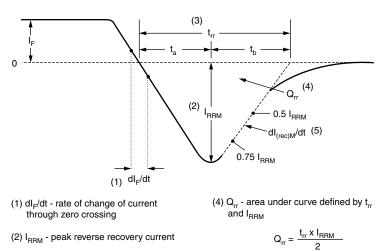


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V_R = 200 V 0.01 L = 70 μH D.U.T. D dl_F/dt adjust IRFP250 G s

Fig. 9 - Reverse Recovery Parameter Test Circuit



Π

(3) t_{rr} - reverse recovery time measured from zero crossing point of negative going I_F to point where a line passing through 0.75 $\mathrm{I}_{\mathrm{RRM}}$ and 0.50 $\mathrm{I}_{\mathrm{RRM}}$ extrapolated to zero current.

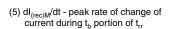


Fig. 10 - Reverse Recovery Waveform and Definitions

V

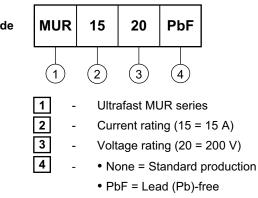
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ORDERING INFORMATION TABLE

Device code



Tube standard pack quantity: 50 pieces

LINKS TO RELATED DOCUMENTS				
Dimensions http://www.vishay.com/doc?95221				
Part marking information	http://www.vishay.com/doc?95224			
SPICE model	http://www.vishay.com/doc?95271			



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